Seat No.:	Enrolment No.
Deat 110	

GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER-VI (NEW) - EXAMINATION - SUMMER 2016

Subject Code:2161102 Date:11/0		/2016	
St	ubject	Name: Advanced Microprocessor	
		10:30 AM to 01:00 PM Total Marks	: 70
In	2.	ons: Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks.	
Q.1	(a) (b)	Write a difference between RISC and CISC Processor Explain addressing mode of ARM Processor with example	07 07
Q.2	(a) (b)	Draw and explain ARM Development tool Explain CPSR and SPSR register	07 07
	(b)	Or Explain ARM processor mode	07
Q.3	(a) (b)	Describe the principle features of ARM Architecture Explain rotate and barrel shifter instruction	07 07
Q.3	(a) (b)	Or Draw and explain 3 stage pipeline Architecture of ARM Processor Write a short note on Binary operators in C language	07 07
Q.4	(a)	Draw interfacing diagram of LPC2148 with LED & switch .write a C program to read the status of switch and get the output of LED .the switch &LED is connected to pin 1.16 & 0.30 respectively	07
	(b)	Draw interfacing diagram of one CC seven segment on port 0.0 to 0.7 .write a C program to display 0 to 9 & A to F with every one second time delay Or	07
Q.4	(a)	Draw and explain interfacing Diagram of relay with LPC 2148 on P0.30 .Write a C program to turn on &off with delay	07
	(b)	Write a operation of following instruction i) LDMIA r0!,{r2-r6} ii) SWPB r2,r1,[r4] iii) STR r0,[r1] iv) RSC r0,r1,r2 v) ADDEQ r0,r1,r2 vi) EORS r1,r3,r0 vii) TST r1,r2	07
Q.5	(a) (b)	Explain the concept of virtual memory Explain the various cache organization Or	07 07
Q.5	(a) (b)	Write a short note on Advanced Microprocessor Bus Architecture (AMBA) Explain exception handling in ARM processor	07 07
