## **GUJARAT TECHNOLOGICAL UNIVERSITY**

BE - SEMESTER-VI (NEW) - EXAMINATION - SUMMER 2016

Subject Code:2162409 Date:11			05/2016	
T	ime: structi	<ol> <li>Attempt all questions.</li> <li>Make suitable assumptions wherever necessary.</li> </ol>	<b>: 7</b> 0	
<b>7</b> 1		3. Figures to the right indicate full marks.  Write A detailed Note On: AC Voltage Regulator	07	
Q.1	(a) (b)	Write A detailed Note On: - AC Voltage Regulator.  Describe working of active front end rectifier. Discuss effect of source inductance on input current waveform.	07	
Q.2	(a) (b)	Explain Square wave & Quasi Square wave Inverter in Details.  What is THD? Explain in detail the concept of Dead band and its importance.  OR	07 07	
	<b>(b)</b>	Compare 1-phase and 3-phase voltage controllers in tabular form.	07	
Q.3	(a) (b)	Write a technical note on: - different methods of Inverter control. Write a short note on SVPWM.	07 07	
Q.3	(a)	OR Compare voltage mode and current mode internal control techniques w.r.t.	07	
Q.S	(b)	Inverter. What is PWM? Explain unipolar SPWM technique. Also compare it with bipolar SPWM technique.	07	
Q.4	(a)	Define the term harmonic. State demerits of it & discuss selective harmonic elimination scheme in brief.	07	
	<b>(b)</b>	Explain L-type ZCS resonant converter with circuit diagram and waveforms.  OR	07	
Q.4	(a)	Define parallel resonance. Explain the concept of parallel resonant inverter circuit with required diagrams.	07	
	<b>(b)</b>	Explain the McMurray-Bedford half-bridge inverter with circuit diagram and Waveforms.	07	
Q.5	(a)	Draw & explain basic series inverter circuit employing load commutation. State limitation of this series inverter.	07	
	<b>(b)</b>	Explain five-level flying capacitor multilevel inverter with circuit diagram and waveforms.	07	
0.5	(a)	OR	Ω'	
Q.5	(a)	Explain applications of multilevel inverter as active & reactive power Compensation.	07	
	<b>(b)</b>	Explain 3 level diode clamped multilevel inverter (DCMLI) with necessary diagrams. Also write merits & demerits of it.	07	
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