

## REGISTRATION FORM

One Week STTP  
on

### Computer Algorithms & Applications

December 29, 2014 – 2, January 2015

LAST DATE FOR REGISTRATION: 23/12/2014

Name : .....

Designation : .....

Department : .....

Highest qualification : .....

Teaching Experience : .....

Institutional address : .....

E - mail : .....

Mobile No. : .....

Accommodation

required : YES/NO

Payment details

Bank : ..... Amount : .....

DD No. : ..... Dated : .....

I shall attend the training programme for the entire duration. I also undertake the responsibility to inform the coordinator sufficiently in advance, in case I am unable to attend the training programme.

Signature of  
Applicant

Signature & SEAL of the  
Head of Department/Institute

### Organizing Committee Chief Patron

**Dr. C. L. Patel**

(Chairman, CVM & Chairman, BoG, BVM)

### Patrons

**Prin. S. M. Patel**    **Prof. J. D. Patel**    **Er. V. M. Patel**  
(Hon. Secretary)    (Hon. I/c, Secretary)    (Hon. Jt Secretary)

**Er. M. J. Patel**    **Shri B. P. Patel**    **Prin. R. C. Desai**  
(Hon. Jt Secretary)    (Hon. Jt Secretary)    (Hon. Jt Secretary)

### President

**Dr. F. S. Umrigar**

(Principal, BVM Engineering College)

### TEQIP-II

**Coordinator - Dr. Darshak G. Thakore**  
**Nodal Officer- Academics, Dr. P. M. George**  
**Nodal Officer- Finance, Prof. S. B. Patel**

### Program Team Members

Prof. S. A. Bakhru | Prof. P. B. Swadas  
Dr. Mayur Vegad | Hemant Vasava |  
B. A. Tanawala | Kirti Sharma | U. K. Jaliya | M. J. Joshi

**Brochure is also available on**  
**[www.bvmengineering.ac.in](http://www.bvmengineering.ac.in)**

### TEQIP-II Sponsored

One Week  
Short Term Training Program  
on

### Computer Algorithms & Applications

29, December 2014 - 2, January 2015



**Organized by**  
**COMPUTER ENGINEERING DEPARTMENT**  
**Dr. Darshak G. Thakore, Professor & Head**

**Program Coordinators**  
**Dr. N. M. Patel**  
**M. I. Hasan**

**Birla Vishvakarma Mahavidyalaya**  
**(Engineering College)**

## About the Institute and Vallabh Vidyanagar



Sixty six years before, Shri Sardar Vallabhbhai Patel kindled a lamp of rural knowledge centre – 'Vallabh Vidyanagar' in Gujarat, and today this centre is a 'Bodhigaya' for over forty thousand young Buddhas studying in varied disciplines at Sardar Patel & Gujarat Technological University. A branch shooted out, from this Bodhigaya tree for engineering education, as Birla Vishvakarma Mahavidyalaya (B V M Engineering College).

This branch was nurtured by two true Ghandhians Er. Bhailalbhhai Patel and Shri Bhikhabhai Patel which has an alumni base of more than 18,000 students.

Vallabh Vidyanagar, located 6 km from MILK City-ANAND is situated along the Ahmedabad - Mumbai railway line and well connected to major cities and towns by rail as well as road. This educational township has over 45 institutes managed by the Charutar Vidya Mandal.

### About the Department

Computer Engineering course was commenced in 1991 and the department of Computer Engineering has been progressing at a rapid pace with the development in terms of infrastructure facilities, upgraded syllabi and learned faculty members. Besides offering B. E. (Computer Engineering) it also offers Post Graduation in Computer Engineering under self- financed scheme. Average experience of faculty member is more than 15 years.

## About the Programme

The study of Algorithms is very important part of research in computer science and engineering. Strong fundamentals of design and analysis of algorithms is highly desirable to pursue research and development activities. The scope of computer algorithms is not only limited to Computer Engineering and Information Technology but rather it is essential for all other branches of engineering and technology. The advancements in algorithms range from development of faster algorithms to new algorithmic paradigms that emerge with thrust in various areas. This workshop is an initiative to strengthen the fundamentals and explore advances in computer algorithms to motivate faculty members towards research.

### Course Contents

- Fundamentals, Asymptotic notations & Algorithm analysis
- Dynamic programming & applications
- Greedy algorithms & applications
- NP-completeness
- Randomized algorithms
- Approximation algorithms
- Linear programming
- Distributed algorithms

### Resource Persons

The resource persons for the program shall include experts from industries and prestigious institutes like IIT, SVNIT, DAIICT, Nirma University.

### Who should attend?

The training program is open for faculty members of Computer Engineering / Information Technology and related disciplines from AICTE approved engineering colleges and Engineers from Industries.

## Registration Details

Registration fees for the programme shall be Rs.1000 for faculty from academic institutions and Rs. 2000 for industry personnel & practicing engineers. Registration fees include registration kit, course materials, breakfast and Lunch. Accommodation in university/institute guest house shall be provided to participants upon prior request on chargeable basis. The participants have to bear their own travelling expenses. The nonrefundable registration fees should be sent through a Demand Draft in favour of "Principal, BVM Engineering College" payable at Vallabh Vidyanagar.

### How to Reach?

Institute is located 4 km from Anand city. It is well connected by bus routes Anand - Vadtal and Anand - Karamsad. Anand is connected by NH -8 and Ahmedabad - Baroda expressway. Anand is also connected with western railway line. Baroda airport is just 50 km and Ahmedabad International Airport is around 80 km from the campus.

### Important Dates:

Last date for receipt of application with DD : 23/12/2014

Information about selection (through e-mail only) : 24/12/2014

Last date of confirmation by participants : 26/12/2014

Since only limited seats are available, interested faculty members are encourage to apply early with the non-refundable deposit.

### For Correspondence:

**Dr. N. M. Patel (M): 9428660842**

**M. I. Hasan (M): 8401042044**

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